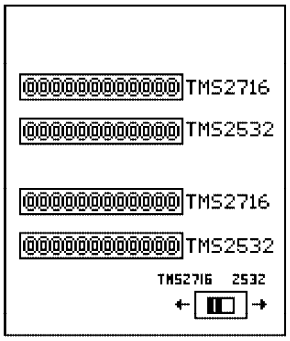
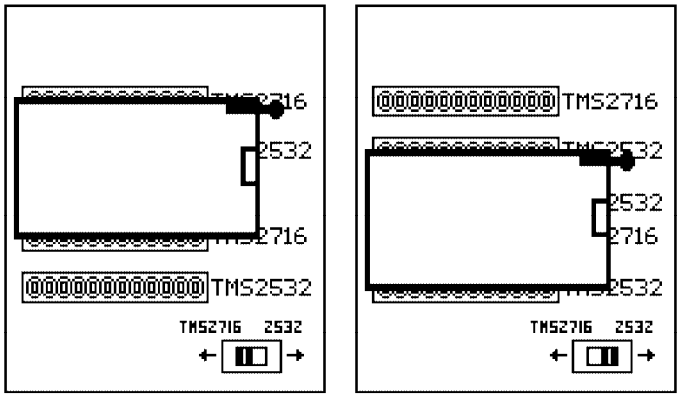
USING THE ATMS1632 COMBINATION ADAPTER



The ATMS1632 adapter allows the Andromeda Research EPROM+ programming system to support both the TMS2716 and TMS2532 memory devices. As both of these parts require non-standard programming voltages or connections, the ATMS1632 adapter addresses this by allowing the user to position the 24 pin adapter socket such that it aligns with two of four rows of receiving sockets on the adapter base (see illustration at right).

To configure the ATMS1632 adapter for proper operation, carefully align the pins on the bottom of the 24 pin socket with the corresponding rows of receiving sockets on the adapter base. The adapter base is marked to show which rows of receiving sockets must be used. Once the 24 pin socket pins are aligned with the proper two rows, firmly press the socket into place. **Note:** Be careful not to bend the 24 pin socket pins. Set the slide switch to the proper position for the selected device. To change the adapter configuration, slightly lift one end of the 24 pin socket, then slightly lift the opposite end in a rocking motion until the socket is free from the adapter base. **Note:** If you lift one end of the 24 pin socket too an extended angle you will bend the bottom pins. Use care when removing the socket.

The illustrations below show the adapter base configured for the TMS2716 (left) and the TMS2532 (right). Note the position of the 24 pin socket and the slide switch for each device.



Installing the adapter into the programming unit

The ATMS1632 adapter has a 28 pin base. To insert the adapter into the 32 pin socket on the programming unit, lift the handle on the 32 pin ZIF socket to about a 45 degree angle. This will release the socket mechanism. Align the pins on the ATMS1632 adapter base such that they are fully left justified (open pins on the 32 pin socket on the right). Gently press the adapter base into the 32 pin socket until it is flush with the top. Release the 32 pin ZIF socket handle to lock the ATMS1632 adapter in place.